



SUPPLEMENTAL APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Number:: 10/792,153
Filing Date:: 03-02-04
Application Type:: Regular
Subject Matter:: Utility
Title:: Method and Apparatus for Design
Verification with Equivalency Check
Attorney Docket Number:: X-1270 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 05
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Robert
Middle Name:: E.
Family Name:: Eccles
Street:: 1575 Montellano Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95120

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
-------------------------------------	-------	--

DOMESTIC PRIORITY INFORMATION

Application: :	Continuity Type::	Parent Application::	Parent Filing Date::
This Applic.	Non-Provisional	60/425,143 <u>60/452,143</u>	03-04-03

ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124